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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/038,958	01/02/2002	David Arnold Luick	ROC920010188US1	2154
7590	07/12/2004			
			EXAMINER	
			LANE, JOHN A	
			ART UNIT	PAPER NUMBER
			2188	
DATE MAILED: 07/12/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/038,958	LUICK, DAVID ARNOLD
	<b>Examiner</b>	<b>Art Unit</b>
	Jack A Lane	2188

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 02 January 2002.
- 2a) This action is FINAL.      2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-33 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-33 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |   |   |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____. | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|   | 6) <input type="checkbox"/> Other: _____.                                   |

## DETAILED ACTION

1. Claims 1-33 are presented for examination. Applicant's representative Mr. Gero McClellan was contacted on 07/08/04 regarding a restriction requirement between Group I claims 1-10, 26-33 drawn to a carry generation circuit and Group II claims 11-25 drawn to gating circuitry for propagate and generate circuitry. An election could not be made at that time. The examiner has examined all claims on the merits given that a single reference teaches most aspects of the invention. However, the examiner maintains the need for a restriction between Group I and Group II inventions. Applicant may wish to voluntarily withdraw the claims of Group II to promote a speedy prosecution. Double patenting would not be an issue in a subsequently filed application since the inventions are distinct.

2. The examiner requests, in response to this Office action, any reference(s) known to qualify as prior art under 35 U.S.C. sections 102 or 103 with respect to the instant claims. That is, any prior art (including any products for sale) similar to the instant claimed invention that could reasonably be used in a 102 or 103 rejection. This request does not require applicant to perform a search. This request is not intended to interfere with or go beyond that required under 37 C.F.R. 1.56 or 1.105. This request may be fulfilled by asking the attorney(s) of record handling prosecution and the inventor(s)/assignee for references qualifying as prior art. A simple statement that the

query has been made and no prior art found is sufficient to fulfill the request. Otherwise, the fee and certification requirements of 37 CFR section 1.97 are waived for those documents submitted in reply to this request. This waiver extends only to those documents within the scope of this request under 37 CFR, section 1.105 that are included in the application's first complete communication responding to this requirement. Any supplemental replies subsequent to the first communication responding to this request and any information disclosures beyond the scope of this request under 37 CFR section 1.105 are subject to the fee and certification requirements of 37 CFR section 1.97. In the event prior art documentation is submitted a discussion of relevant passages, figs. etc. is requested. **A response to this inquiry is greatly appreciated.**

The examiner also requests, in response to this Office action, support be shown for language added to the claims on amendment. That is, indicate support for newly added claim language by specifically pointing to page(s) and line no(s). in the specification and/or drawing figure(s). This will assist the examiner in prosecuting the application.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-33 are rejected under 35 U.S.C. § 102(b) as being anticipated by Ngo et al. (Pat. No. 5,964,827).

Ngo et al. (5,964,827) teaches the claimed “first and second numbers” as A and B shown in figures 2 and 3. The claimed step of “generating generate terms and propagate terms” correspond to propagate and generate terms P4, G4 shown in figures 2 and 3 and discussed at col. 3, lines 22-24. The claimed “combined terms” corresponds to terms P16 and G16. The claimed step of “generating the carry from generate terms, propagate terms, and combined terms” corresponds to carry-out Cout shown in figure 6.

As per claims 2 and 3, combined terms G16(1), G16(17), G1633), G16(49) etc. are generated from generate terms G4(0), G4(4), G4(8), G4(12) etc.

As per claim 4, the gating circuitry receiving signals G4(4) and G4(8) is essentially an ORing function.

As per claims 11, 15, 18 and 22, the claimed “first and second gates” correspond to transistors receiving A and B signals. Applicant should note transistor gates are easily configured to function as logic gates.

The examiner believes all dependent claim features are expressly or inherently taught by Ngo. The dependent claim features, while part of the invention, appear to be well known and their relevance not essential to the main invention found in the independent claim(s). Thus, a detailed discussion of the well known claim feature(s) is not warranted at this time.

Applicant should review the entire patent for claimed features not specifically discussed above.

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Applicant should review the prior art not relied upon for its relevance to the instant claims.

Grundland (4,584,661), Young (5,047,974), Fletcher (2003/0115237), Beck et al. (3,993,891), Miura (4,218,747), Cohn 95,122,982) and Edmondson (5,278,783) teach some form of a carry look-ahead adder.

**Any response to this action should be mailed to:**

Under Secretary of Commerce for Intellectual Property and Director  
of the United States Patent and Trademark Office  
PO Box 1450  
Alexandria, VA 22313-1450

**or faxed to:**

(703) 872-9306, (for Official communications intended for entry)

**Or:**

(703) 872-9306, (for Non-Official or draft communications, please  
label "Non-Official" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121  
Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).

Art Unit: 2188

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jack A. Lane whose telephone number is 703 305-3818. The examiner can normally be reached on Mon-Fri from 7:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 703 306-2903.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 305-3900.

JACK A. LANE  
PRIMARY EXAMINER